



IPW

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Confirmation No.: 7790
SHIMIZU Art Unit: 2818
Serial No.: 10/629,809 Examiner: G. Auduong
Filed: July 30, 2003 Docket No.: 108397-00107
For: SEMICONDUCTOR MEMORY HAVING HIERARCHICAL BIT LINE
STRUCTURE (As Amended)

AMENDMENT UNDER 37 C.F.R. § 1.111

Introductory Comments

Director of the U.S. PTO
P.O. Box 1450
Alexandria, Virginia 22313-1450

November 8, 2004

Sir:

In response to the Office Action dated August 10, 2004, please amend the above-titled application as follows: